6676076 2020260

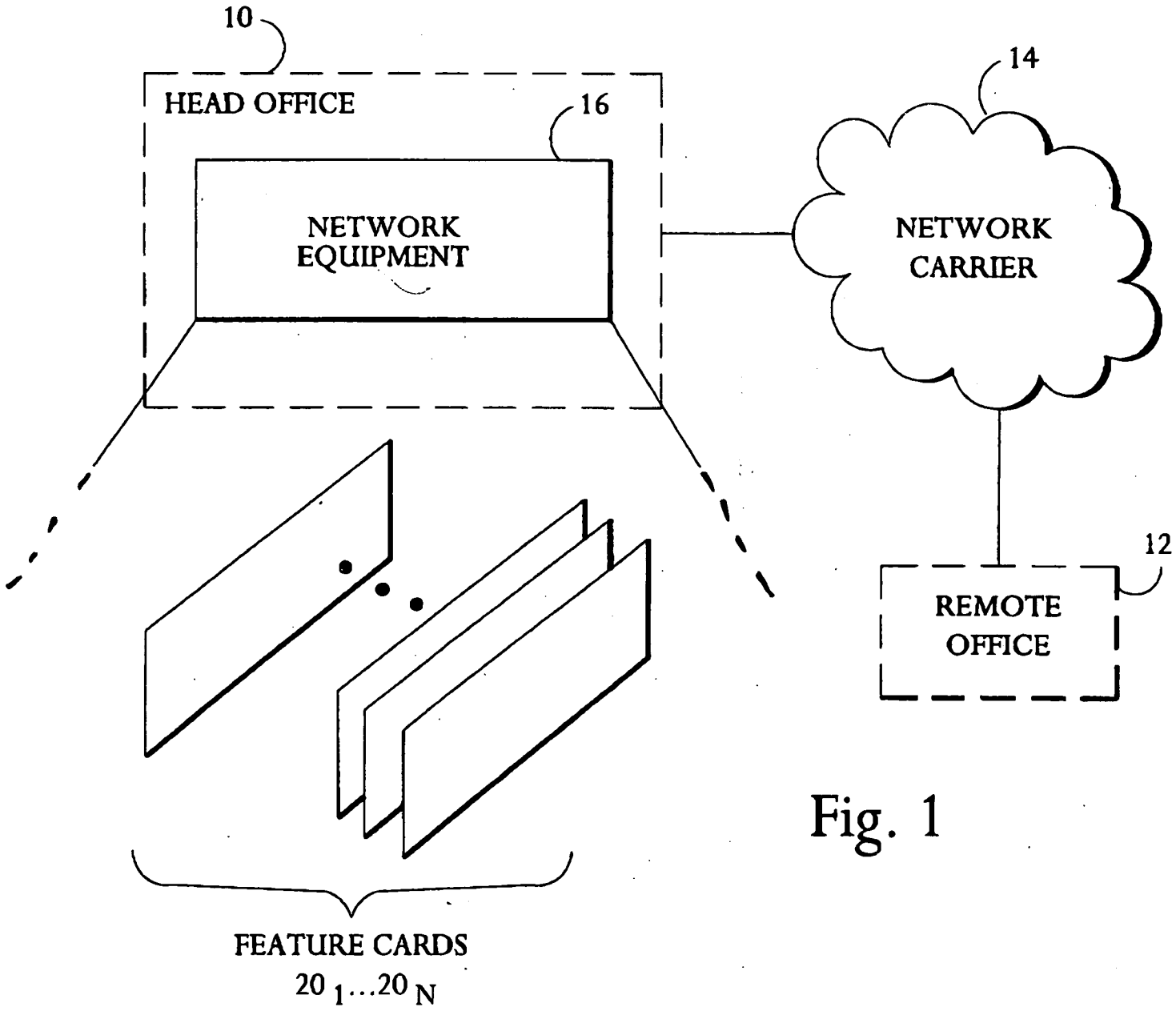

Fig. 1

66ster 2626260

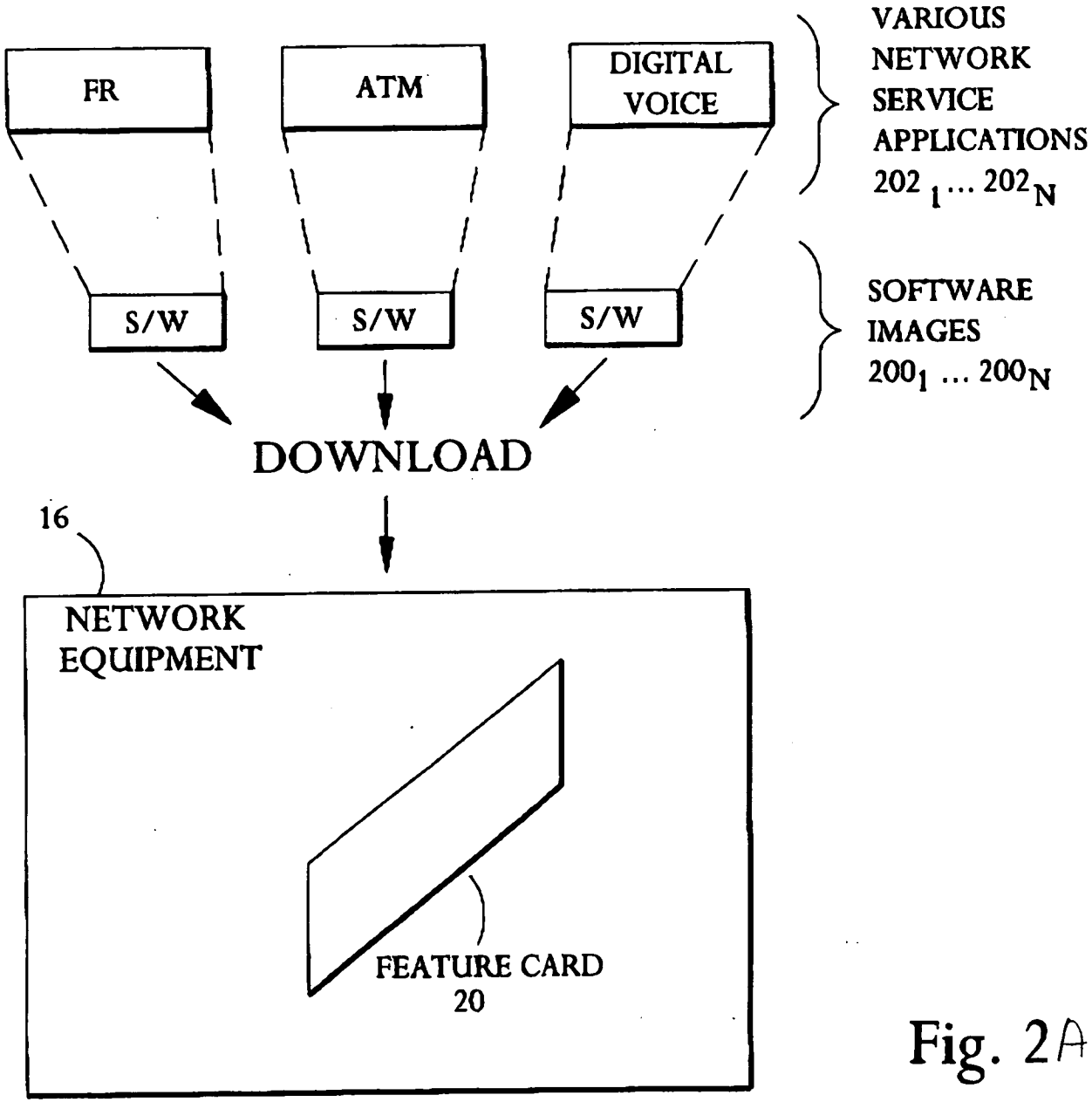

Fig. 2A

6676607 2020260

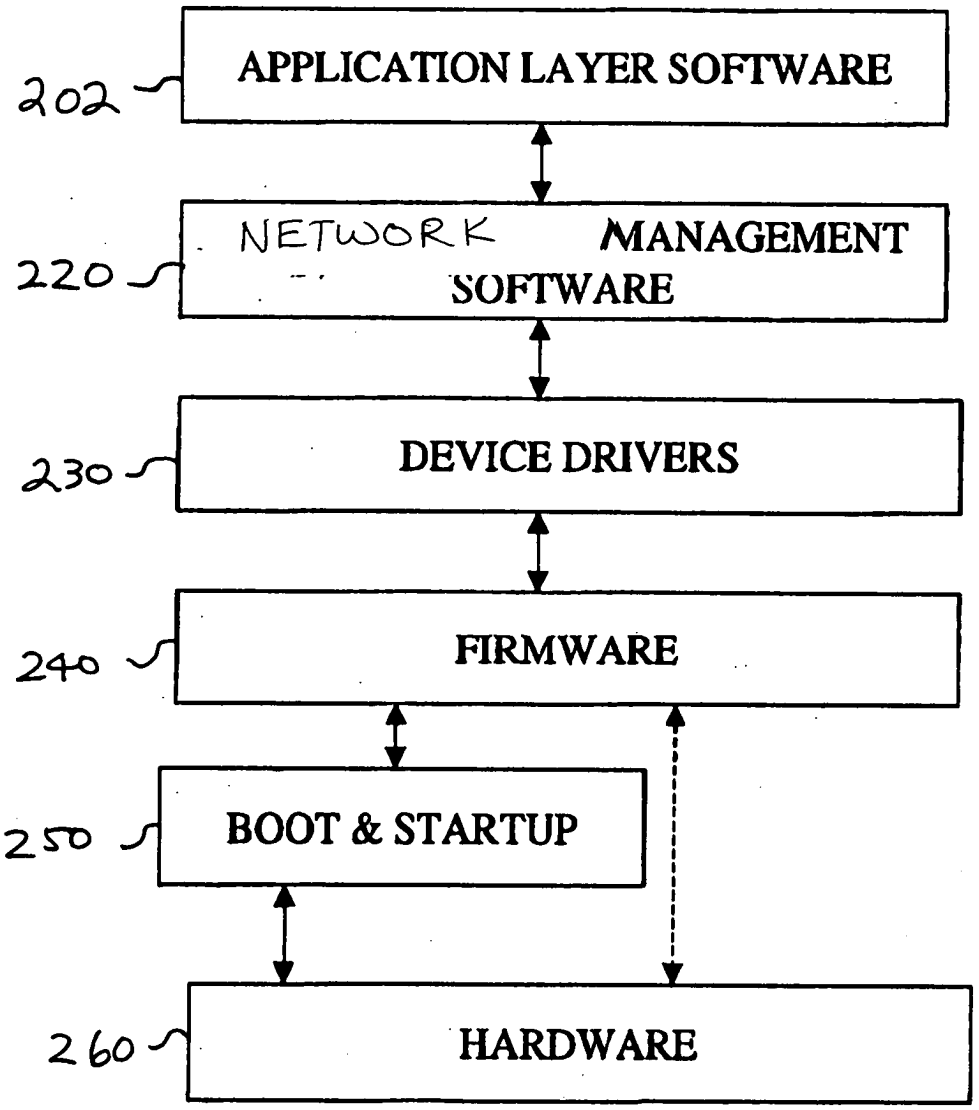

FIG. 2B

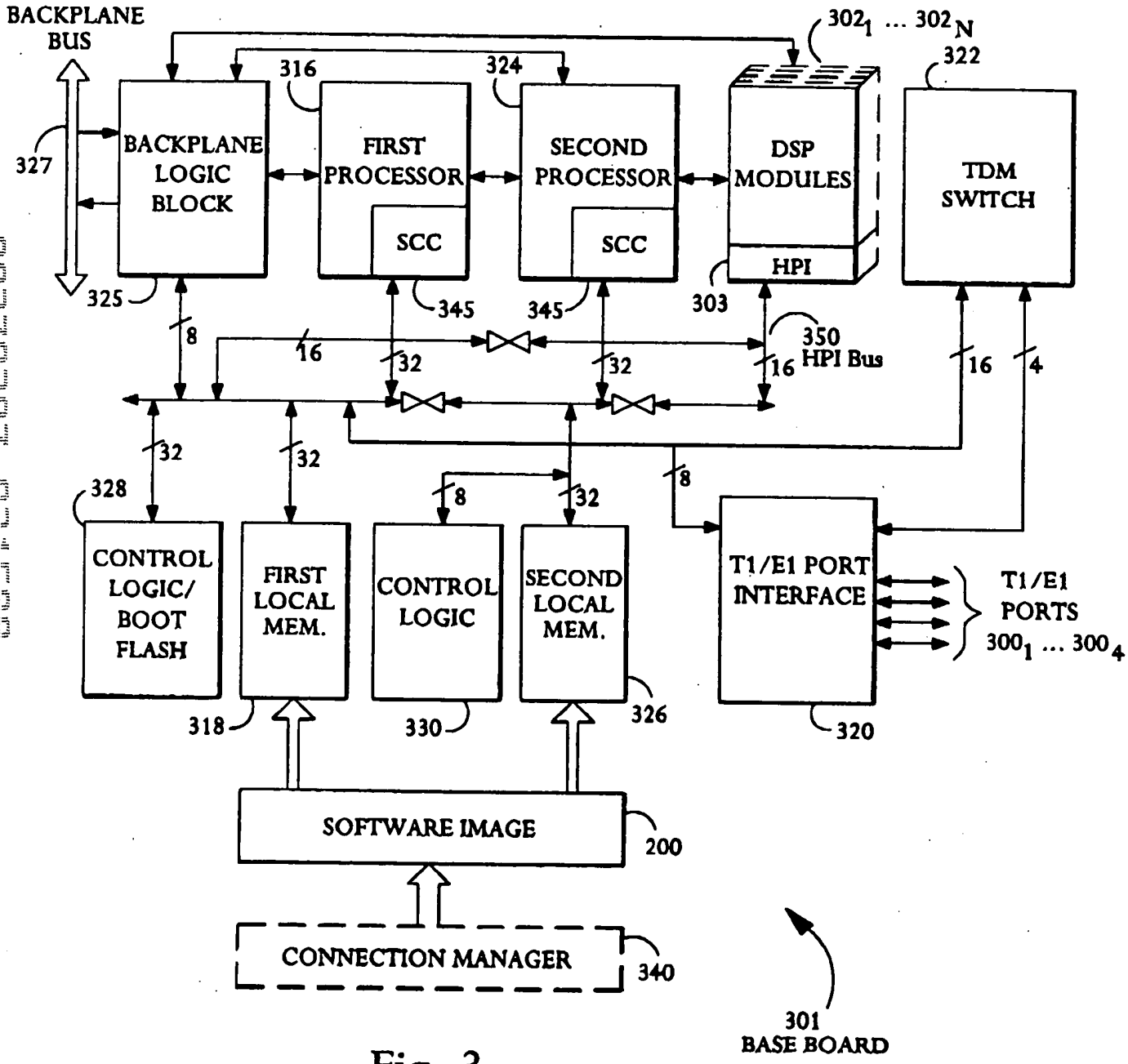
BACKPLANE BUS
327
BACKPLANE LOGIC BLOCK
325
316
FIRST PROCESSOR
SCC
345
324
SECOND PROCESSOR
SCC
345
302$_1$ ... 302$_N$
DSP MODULES
HPI
303
322
TDM SWITCH
350
HPI Bus
8
16
32
32
16
16
4
32
32
328
CONTROL LOGIC/ BOOT FLASH
FIRST LOCAL MEM.
8
32
CONTROL LOGIC
SECOND LOCAL MEM.
8
T1/E1 PORT INTERFACE
T1/E1 PORTS
300$_1$ ... 300$_4$
318
330
326
320
SOFTWARE IMAGE
200
CONNECTION MANAGER
340
301
BASE BOARD

Fig. 3

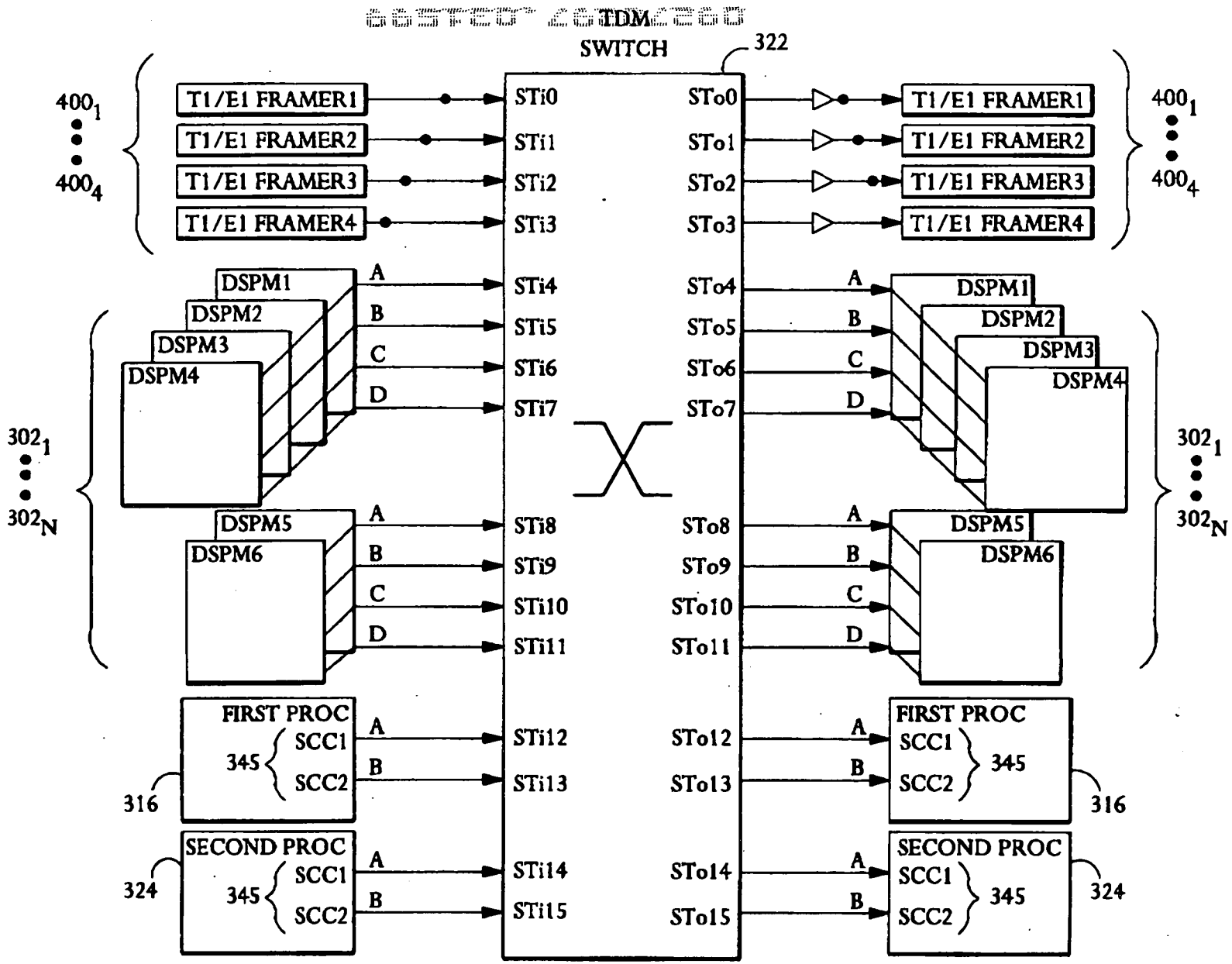
TDM SWITCH
322
400$_1$
400$_4$
T1/E1 FRAMER1
T1/E1 FRAMER2
T1/E1 FRAMER3
T1/E1 FRAMER4
STi0
STi1
STi2
STi3
STo0
STo1
STo2
STo3
DSPM1
DSPM2
DSPM3
DSPM4
DSPM5
DSPM6
A
B
C
D
STi4
STi5
STi6
STi7
STo4
STo5
STo6
STo7
STi8
STi9
STi10
STi11
STo8
STo9
STo10
STo11
302$_1$
302$_N$
FIRST PROC
SECOND PROC
345
SCC1
SCC2
316
324
STi12
STi13
STi14
STi15
STo12
STo13
STo14
STo15

Fig. 4

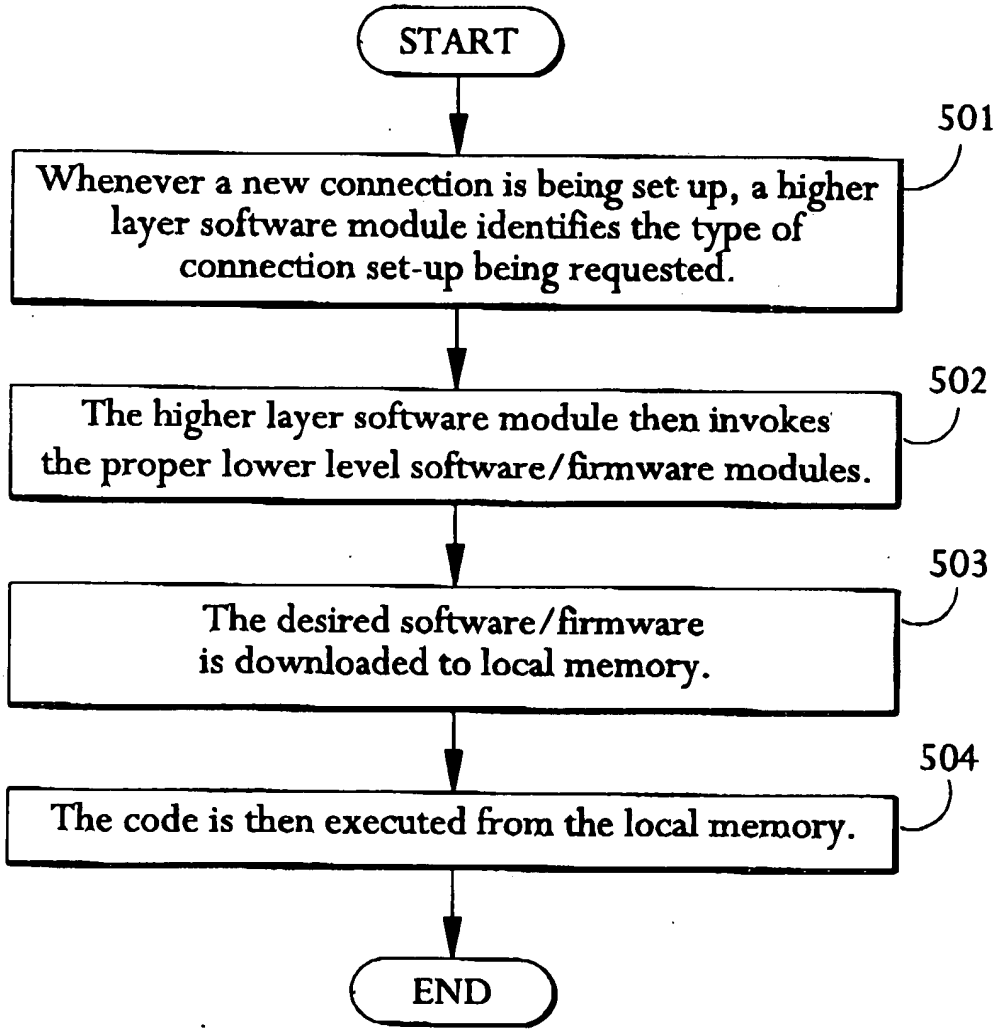
START
501
Whenever a new connection is being set up, a higher layer software module identifies the type of connection set-up being requested.
502
The higher layer software module then invokes the proper lower level software/firmware modules.
503
The desired software/firmware is downloaded to local memory.
504
The code is then executed from the local memory.
END

Fig. 5